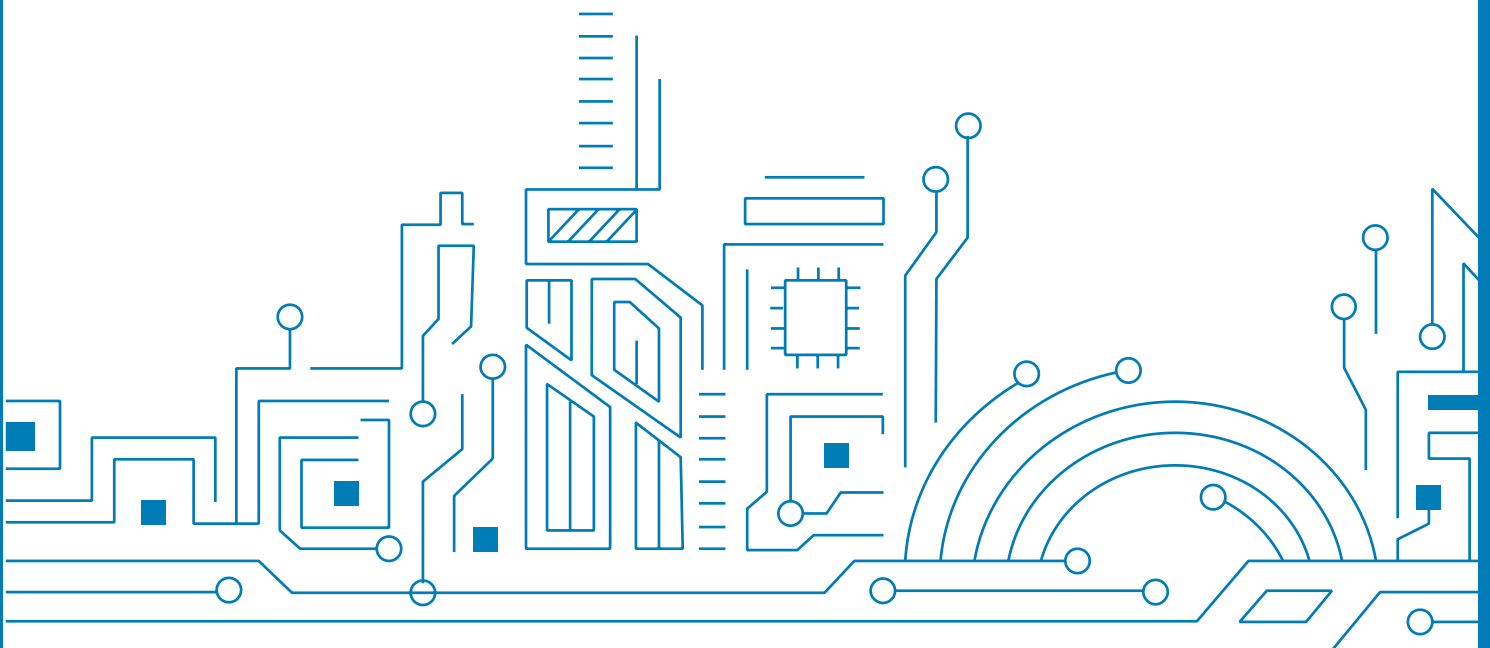Multi-Band GNSS Positioning Module

TAU1202 TAU1205

Datasheet V1.4

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1   SYSTEM OVERVIEW

1.1   Overview

TAU1202/TAU1205 is a high-performance dual-frequency GNSS positioning module, which is based on the state of the art CYNOSURE III architecture. It supports GPS, BeiDou, GLONASS, Galileo, NAVIC/IRNSS, and QZSS).

TAU1202/TAU1205 integrates efficient power management architecture, while providing high precision, high sensitivity and low power GNSS solutions which make it suitable for navigation applications on automotive and consumer electronics, as well as fleet management.

1.2   Features

- Supports all civil GNSS systems
- Supports BDS-3 signal: B1C and B2a
- Concurrent reception of L1 and L5 band signals
- Sub-meter position accuracy, superior in multipath mitigation and lower noise in city valley
- Smart jammer detection and suppression
- Highly integrated module, the best cost-effective high precision solution
- Supports single IRNSS mode (TAU1205)

1.3   Module photo

Figure 1 TAU1202/TAU1205 module photo

Table 1 Differences between TAU1202/TAU1205-1010A00 and TAU1202/TAU1205-1010C00

PN	Description
TAU1202/TAU1205-1010A00	RF_IN pin does not provide the power for active antenna, so a bias choke from ANT_BIAS to RF_IN pin should be used.
TAU1202/TAU1205-1010C00	A bias choke is built-in between ANT_BIAS and RF_IN pin to power the active antenna. If the active antenna is powered by a supply source other than the module, a capacitor should be used to block the DC from RF_IN.

1.4   Block diagram

TAU1202/TAU1205
(A00)

TAU1202/TAU1205
(C00)
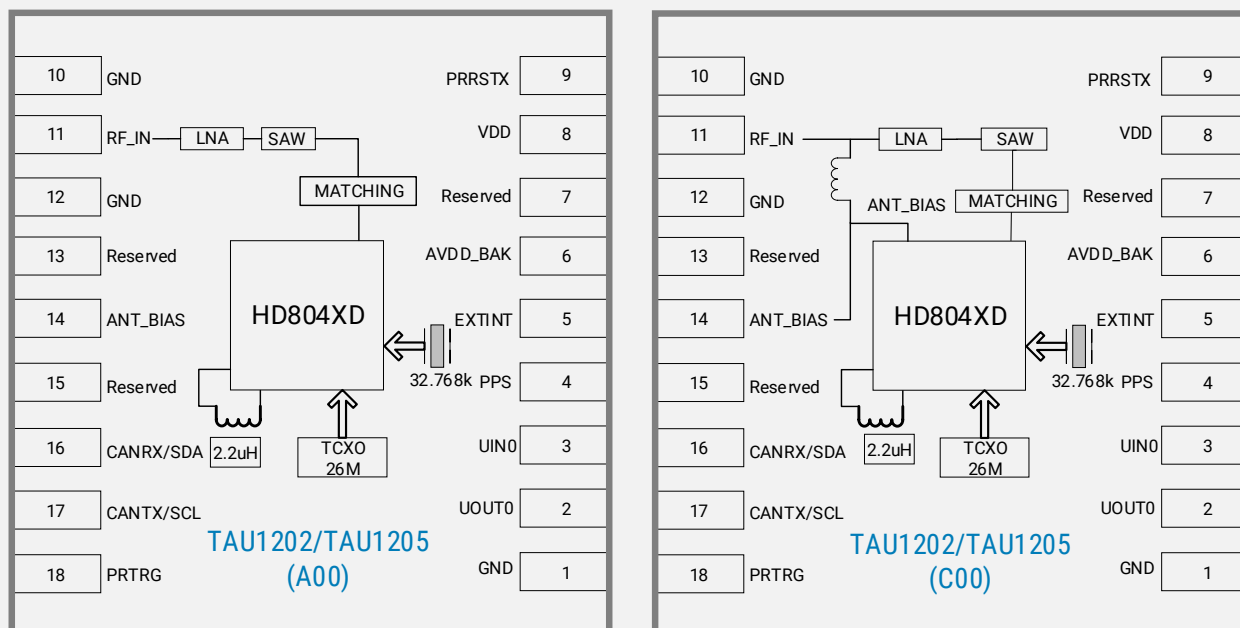
Figure 2 Block diagram

1.5   Specifications

Table 2 Specifications

Parameter	Specification	
GNSS Tracking channel	40 channels	
GNSS reception	TAU1202	GPS/QZSS: L1C/A, L5C
		BDS: B1I, B2a, B1C[1]
		GLONASS: L1OF
		Galileo: E1, E5a
	TAU1205	GPS/QZSS: L1C/A, L5C
		BDS: B1I, B2a
		Galileo: E1, E5a
		IRNSS: L5
Update rate	Maximum 10Hz	
Position accuracy [2]	GNSS	<1m CEP
Velocity & Time accuracy	GNSS	0.1m/s CEP
	1PPS	20ns
Time to First Fix(TTFF)	Hot start	1 sec
	Cold start	24 secs
Sensitivity	Cold start	-148dBm
	Hot start	-155dBm
	Reacquisition	-158dBm
	Tracking & navigation	-161dBm

Operating limit	Velocity	515 m/s
	Altitude	18,000 m
Safety supervision	Antenna short circuit and open circuit detection, and short circuit protection	
	Low voltage detection	
Serial interface	UART	1
	I2C	1
	CAN [3]	1
Protocol	NMEA 0183 Protocol Ver. 4.00/4.10, Cynosure GNSS Receiver Protocol	
Operating condition	Main voltage	1.8 ~ 3.6V
	Digital I/O voltage	1.8 ~ 3.6V
	Backup voltage	1.8 ~ 3.6V
Power consumption	GPS+QZSS, L1 band	22mA@3.3V
	GNSS, L1+L5 band	41mA@3.3V
	Standby	12uA
Operating temperature	-40 °C ~ +85 °C	
Storage temperature	-40 °C ~ +85 °C	
Package	10.1mm x 9.7mm x 2.5mm 18-pin stamp hole	
Certification	RoHS & REACH	

*   [1] Supported by specific firmware.

*   [2] Open sky, dual band, demonstrated with a good external LNA

*   [3] Only customized firmware supported

2   PIN DESCRIPTION

2.1   Pin assignment

10	GND	PRRSTX	9
11	RF_IN	VDD	8
12	GND	Reserved	7
13	Reserved	AVDD_BAK	6
14	ANT_BIAS	EXTINT	5
15	Reserved	PPS	4
16	CANRX/SDA	UIN0	3
17	CANTX/SCL	UOUT0	2
18	PRTRG	GND	1

TAU1202/TAU1205
Top View

Figure 3 Pin assignment (top view)

2.2   Detailed pin descriptions

Table 3 Detailed pin descriptions

Function	Symbol	No.	I/O	Description
Power	VDD	8	Power	Main supply input.
	GND	1,10,12	VSS	Ground
	AVDD_BAK	6	Power	Backup supply input.
Antenna	RF_IN	11	I	RF signal input. Use a controlled impedance of 50Ω from connect RF_IN to the antenna or the antenna connector.
	ANT_BIAS	14	O	RF section output voltage. Used to power the external active antenna. The current is limited below 35mA.
UART	UOUT0	2	O	UART0 serial data output.
	UIN0	3	I	UART0 serial data input.
I^2C/CAN	CANRX/SDA	16	I/O	I^2C data transmission, or CAN data input. Leave it floating if not used.
	CANTX/SCL	17	I/O	I^2C clock, or CAN data output. Leave it floating if not used.
System	PRTRG	18	I	Mode selection, or the trigger input in deep sleep mode to wake up the system
	PRRSTX	9	I	External reset, low active
	PPS	4	O	Time pulse output (PPS)
	EXTINT	5	I	GPIO, Default (EXTINT): a trigger pin to external interrupt, leave it floating if not used.
Reserved	Reserved	7,13,15,	--	Reserved, leave it floating if not used

3   ELECTRICAL CHARACTERISTICS

3.1   Absolute Maximum Rating

Table 4 Absolute rating

Symbol	Parameter	Min.	Max.	Unit
VDD	Power input for the main power domain	-0.5	3.63	V
AVDD_BAK	Power input for the backup power domain	-0.5	3.63	V
$T_{storage}$	Storage temperature	-40	85	°C
T_{solder}	Solder reflow temperature	--	260	°C

3.2   IO Characteristics

3.2.1   PRRSTX and PRTRG

Table 5 PRRSTX and PRTRG

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
I_{IZ}	Input leakage current	--	--	--	+/-1	uA
V_{IH}	Input high voltage	--	AVDD_BAK*0. 7	--	AVDD_BAK	V
V_{IL}	Input low voltage	--	0	--	AVDD_BAK*0.3	V
C_i	Input capacitance	--	--	--	10	pF
R_{PU}	Pull-up resistance	--	18	--	84	kOhm

3.2.2   ANT_BIAS

Table 6 ANT_BIAS

Parameter	Condition	Min.	Typ.	Max.	Unit
RF supply current	--	--	--	35	mA
RF supply voltage	--	1.42	--	3.43	V

3.2.3   Others

Table 7 Others

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
I_{IZ}	Input leakage current	--	--	--	+/-1	uA
V_{IH}	Input high voltage	--	VDD*0.7	--	VDD	V
V_{IL}	Input low voltage	--	0	--	VDD*0.3	V
V_{OH}	Output high voltage	I_{OH}=11.9 mA, VDD=3.3V	2.64	--	--	V
		I_{OH}=2.8 mA, VDD=1.8V	1.53	--	--	V
V_{OL}	Output low voltage	I_{OL}=7.9 mA, VDD=3.3V	--	--	0.4	V
		I_{OL}=3.9 mA, VDD=1.8V	--	--	0.45	V
C_i	Input capacitance	--	--	--	11	pF
R_{PU}	Pull-up resistance	-	35	--	84	kOhm

3.3   DC Characteristics

3.3.1   Operating Conditions

Table 8 Operating conditions

Symbol	Parameter	Min.	Typ.	Max.	Unit
VDD	Power input for the main power domain	1.8	3.3	3.6	V
AVDD_BAK	Power input for the backup power domain	1.8	3.3	3.6	V
ICC_{max}	Maximum operating current @ VDD	--	--	200	mA
T_{env}	Operating temperature	-40	--	85	°C

3.3.2   Power Consumption

Table 9 Power consumption

Symbol	Parameter	Measure Pin	Typ.	Unit
I_{CCRX1}[1]	Run Mode (GPS+QZSS, L1 only)	VDD[3]	22	mA
I_{CCRX2}[2]	Run Mode (GNSS, L1+L5)	VDD[3]	41	mA
I_{CCDBM}	Standby mode	AVDD_BAK[4]	12	uA

*   [1] GPS+QZSS, L1 band only, 16 tracking channels, position fixed

*   [2] All GNSS, L1 + L5 band, 32 tracking channels, position fixed

*   [3] Condition: VDD=3.3V@Room Temperature; All Pins Open.

*   [4] Condition: AVDD_BAK=3.3V@Room Temperature; All Pins Open.

4 HARDWARE DESCRIPTION

4.1 Connecting power

TAU1202/TAU1205 positioning module has two power supply pins: VDD and AVDD_BAK. The main power is supplied through the VDD pin, and the backup power is supplied through the AVDD_BAK pin. In order to ensure the positioning performance, please control the ripple of the module power supply less than 50m Vpp. It is recommended to use the LDO above 200mA current.

If the power for VDD pin is off, the real-time clock (RTC) and battery backed RAM (BBR) are supplied through the AVDD_BAK pin. Thus, orbit information and time can be maintained and will allow a Hot or Warm start. If no backup battery is connected, the module performs a cold start at every power up if no aiding data are sent to the module.

Note: If no backup supply is available, connect the AVDD_BAK pin to VDD or leave it floating.

4.2 Power on/off Sequence

TAU1202/TAU1205 has two independent power domains (backup and main domain). In data backup mode, main power supply can be completely shut down for further power reduction for ultra-low power application.

4.2.1 Initial system power on

When both backup and main supply power on from their off state, external reset (PRRSTX) must be active and hold more than 5ms after both backup supply and main supply reach the minimum operating voltage. Initial system power on sequence is illustrated in Figure 4.
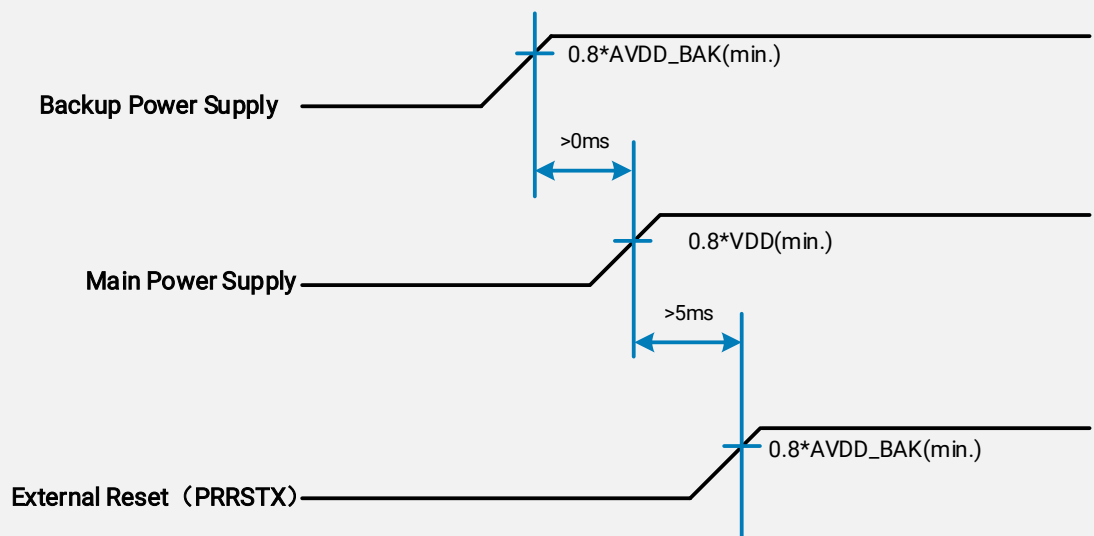

Figure 4 Initial system power on sequence

4.2.2 Main power supply off/on in application

If application intends to shut down main power supply (VDD) while keep backup power supply

(AVDD_BAK) alive to save backup data, the following rules should be applied:

External reset (PRRSTX) must be active when main power supply is under power off. In this case, external reset must be hold active more than 5ms after main power supply resumes to minimum operating voltage. Main power on sequence in application is illustrated in Figure 5.
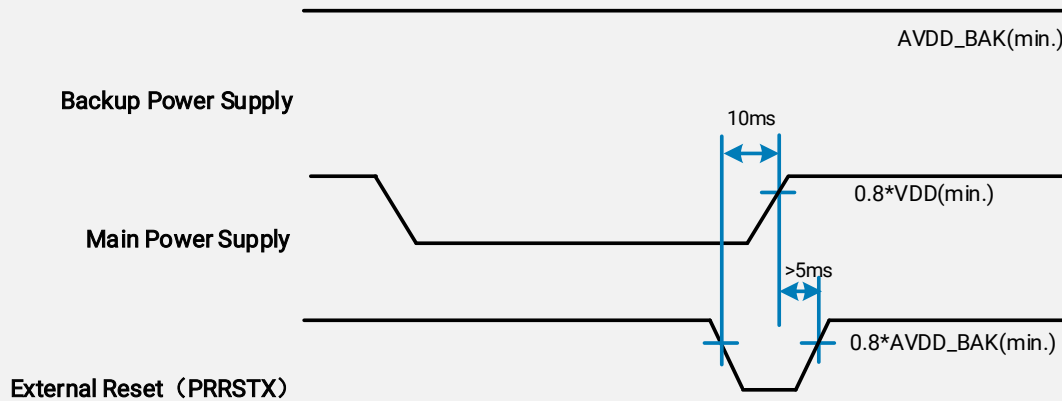

Figure 5 Main power on sequence

4.3   Antenna design

There is built-in LNA and SAW in the GNSS module. It is recommended to use an active antenna with gain less than 36dB and noise figure less than 1.5dB.

The module has built-in short circuit detection and open circuit detection function, which can detect the status of normal connection, and send out antenna status prompt message in NMEA data.

- Short circuit protection
  - » The module includes internal short circuit antenna detection. Once an overcurrent is detected at the ANT_BIAS port, the module will restrict current output automatically to protect from damages.
- Open circuit detection
  - » The module can detect an open circuit in the antenna. Users can judge it from antenna status messages.

Table 10 ANT_BIAS current range and antenna status

Antenna status	Status output	ANT_BIAS current range
Open circuit	OPEN	0< ANT_BIAS ≤ 1mA
Regular circuit or open circuit	OK or OPEN	1mA< ANT_BIAS ≤ 2mA
Regular circuit	OK	2mA< ANT_BIAS ≤ 40±5mA[1]
Short circuit	SHORT	40±5mA[1] < ANT_BIAS < 55mA

*   [1] ±5mA are differences between product batches.

TIPs：
1. Pulse width of the minimum detectable overshoot current should be more than 10uS.
2. NMEA message of antenna status output:

- OPEN: $GNTXT,01,01,01,**ANT_OPEN***40
- OK: $GNTXT,01,01,01,**ANT_OK***50
- SHORT: $GNTXT,01,01,01,**ANT_SHORT***06

4.4   Reset and mode control

The operation mode of GNSS module is controlled by PRRSTX (nRESET) and PRTRG (BOOT) pin. While the module works in normal operation, leave PRRSTX and PRTRG pins floating if there is no upgrading or reset demands, or others.

- Keep PRTRG pin floating during system power-up or the external reset (PRRSTX from low to high), and the module will enter **User Normal Mode**.
- When the module powers up or PRRSTX from low to high, the module will execute an **external reset**. (If the power for AVDD_BAK is always on, the external reset will not affect the ephemeris data in the backup domain)
- Drive PRTRG pin to low or connect PRTRG to GND directly (not by pull-down resistance) during system power-up or the external reset (PRRSTX from low to high), and the system enters **BootROM Command Mode** at PRTRG pin being released from low to floating state, and ready for firmware upgrading command.
- When connecting PRRSTX and PRTRG to any host IO, DO NOT use the pull-up or pull-down resistance.

4.5   Serial interfaces

The module provides a TTL Universal Asynchronous Receiver / Transmitter (UART) interface. The data format is: 1 start bit, 8 data bits, 1 stop bit, no checksum, and the default baud rate is 115200 bps. NMEA data outputs while the module is powered on.

When the module is applied to the specific application, users can shut off the main power in order to further reduce the power consumption. To avoid the high level in serial interface influencing the normal operation, it is highly suggested to cut off the serial port when shut off the main power. Otherwise, please set the serial port to input mode or high impedance state with pull-down resistor.
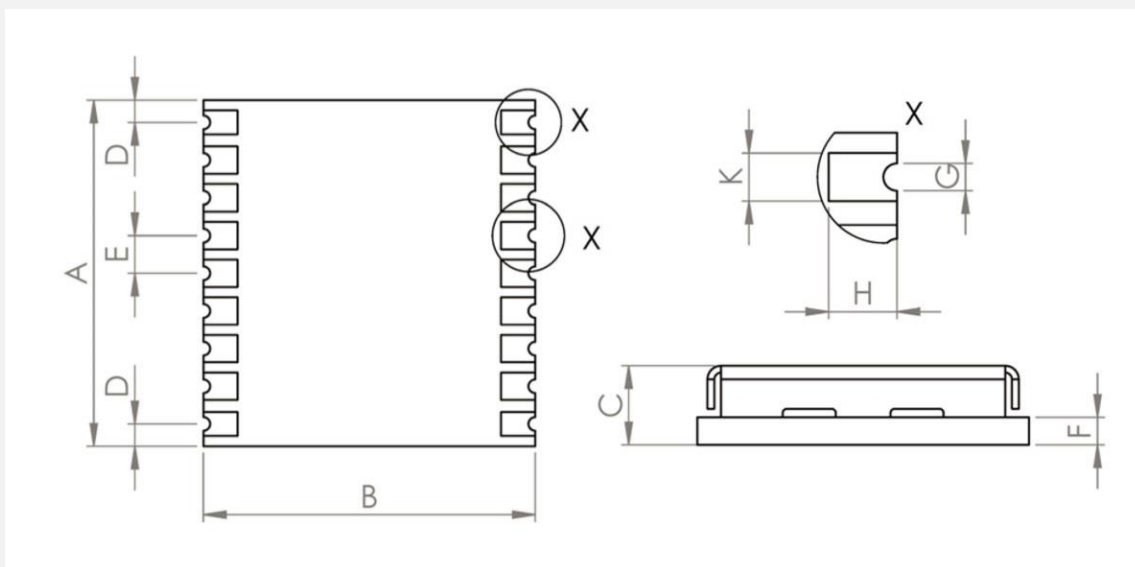
5   MECHANICAL SPECIFICATION

Figure 6 Dimensions

Table 11 Dimensions

Symbol	Min. (mm)	Typ.(mm)	Max. (mm)
A	9.9	10.1	10.3
B	9.5	9.7	9.9
C	2.3	2.5	2.7
D	0.55	0.65	0.95
E	1.0	1.1	1.2
F	0.6	0.8	--
G	0.4	0.5	0.6
H	0.8	0.9	1.0
K	0.7	0.8	0.9

6   REFERENCE DESIGN

6.1   Minimal Design

TAU1202/TAU1205 includes two versions of design (A00 and C00).

- In A00 design, RF_IN pin does not provide the power for active antenna, so a bias choke of 82nH from ANT_BIAS to RF_IN pin should be used. (Refer to **Figure 7**)

- In C00 design, a bias choke is built-in between ANT_BIAS and RF_IN pin to power the active antenna. (Refer to **Figure 8**)

- If the active antenna is powered by a supply source other than the module, a capacitor should be used to block the DC from RF_IN. (Refer to **Figure 9**)

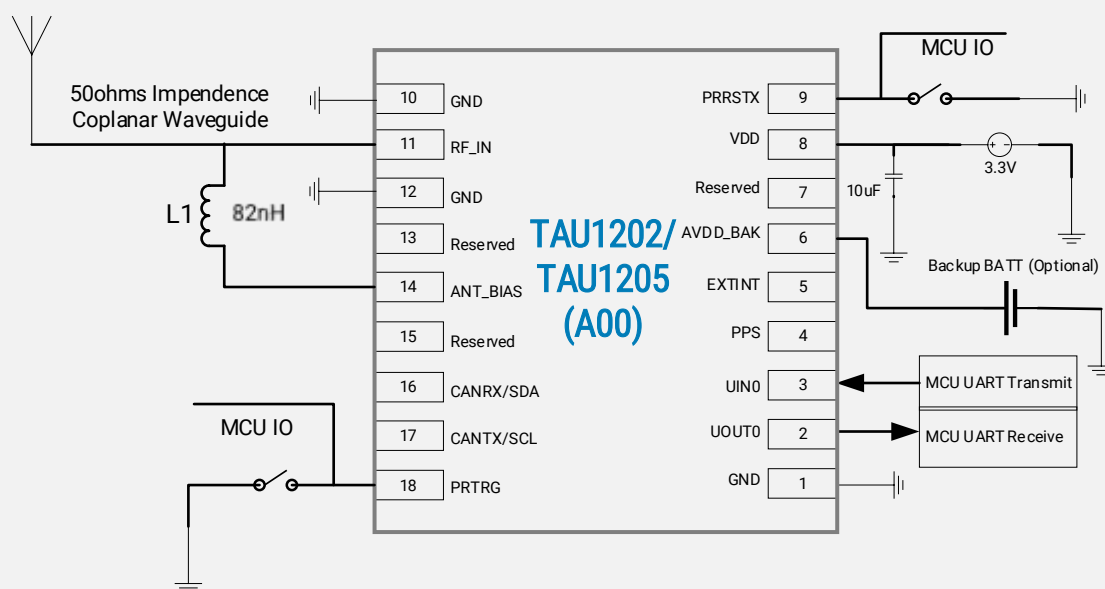The characteristic impedance from RF_IN pin to the antenna connector should be 50Ω.

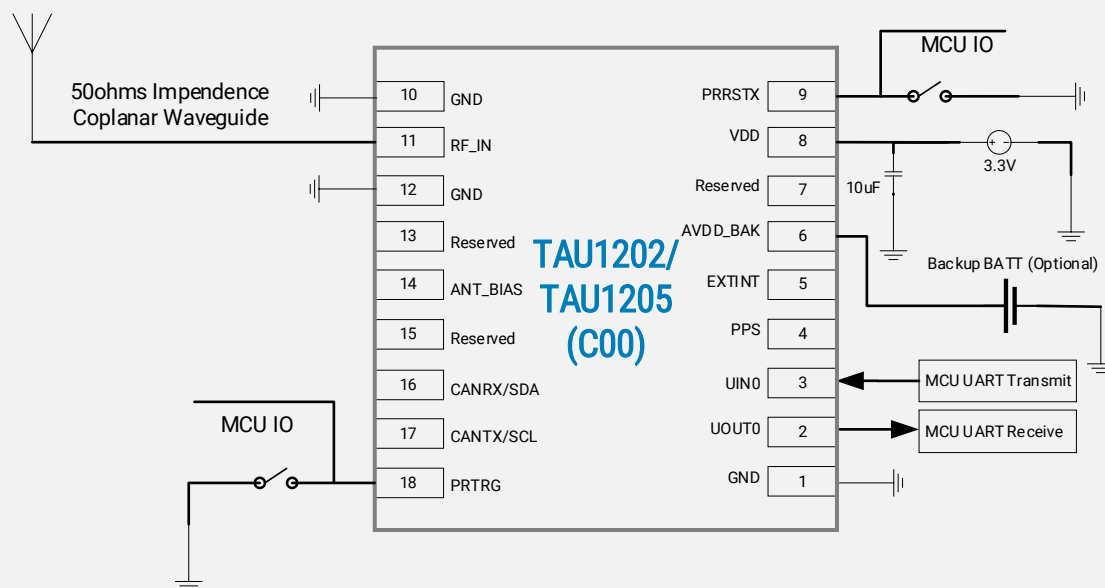**Figure 7 Minimal application diagram of A00 with internal power for ANT**

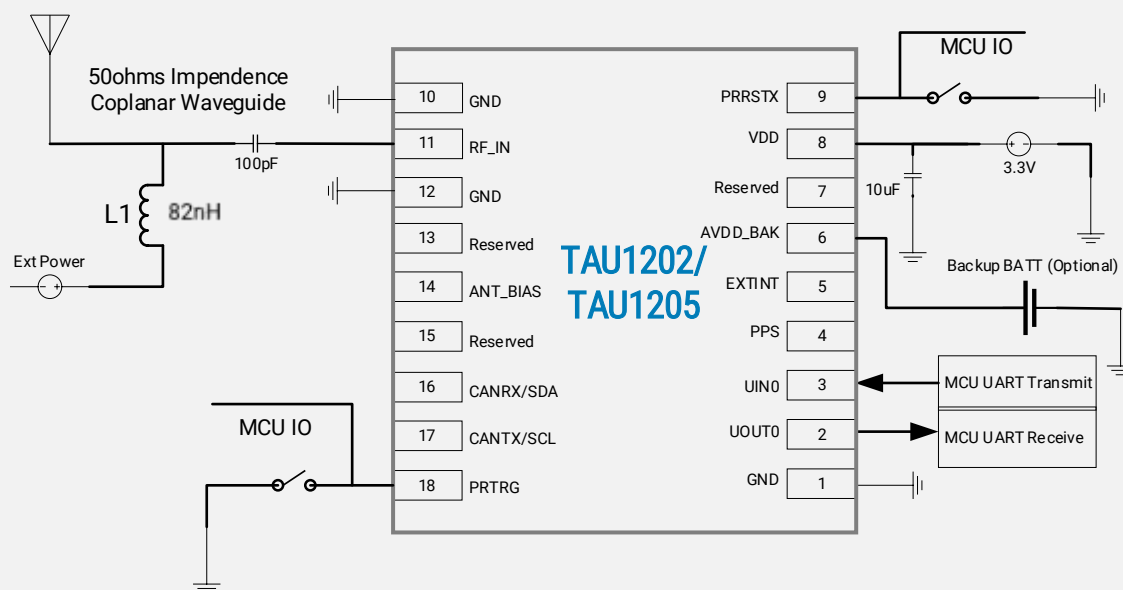**Figure 8 Minimal application diagram of C00 with internal power for ANT**

Figure 9 Minimal application diagram with external power for ANT
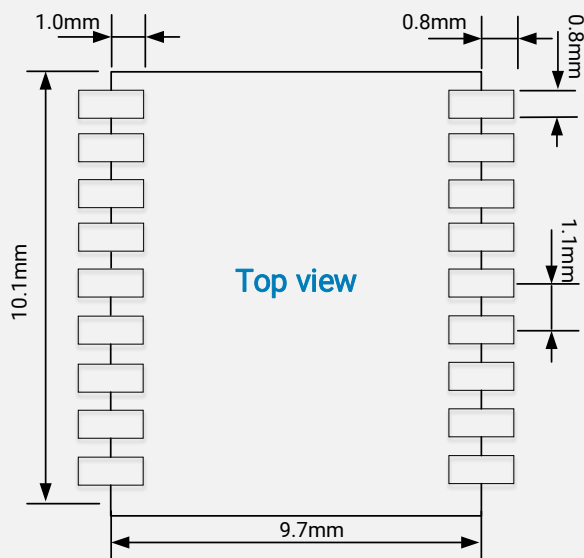
6.2   PCB Footprint Reference

Figure 10 PCB Footprint Reference

6.3   Layout Notes

(1) A decoupling capacitor should be placed close to VDD pin of the module, and the width of power routing should be more than 0.5mm.

(2) The width of RF routing between RF port to antenna interface should be wider than 0.2mm. The characteristic impedance of RF routing between RF port to antenna interface should be controlled to 50Ω.

(3) It is recommended that the routing from RF port to antenna interface refers to the second layer, and no routing are recommended on the layer.

(4) Do not place the module close to any EMI source, like antenna, RF routing, DC/DC or power conductor, clock signal or other high-frequency switching signal, etc.
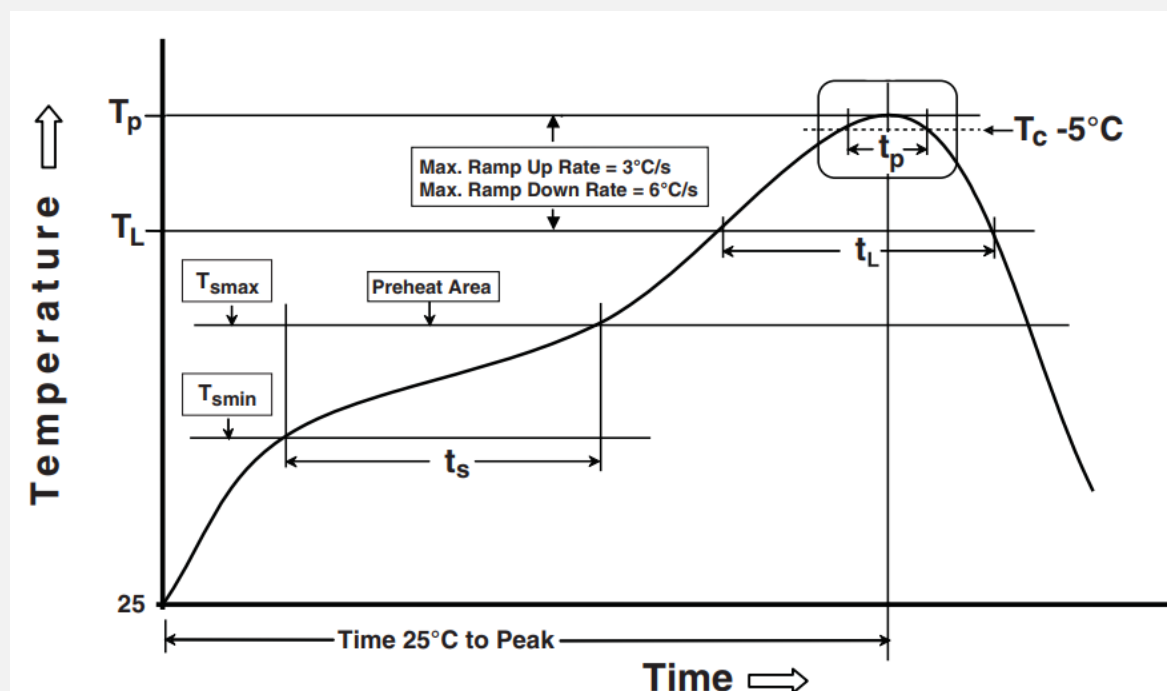
7   REFLOW SOLDERING

Table 12 Reflow profile features

Profile Feature	Pb-Free Assembly
Preheat/Soak	
Temperature Min (T_{smin})	150 °C
Temperature Max (T_{smax})	200 °C
Time (t_s) from (T_{smin} to T_{smax})	60-120s
Ramp-up rate (T_L to T_p)	3 °C/second max.
Liquidous temperature (T_L)	217 °C
Time (t_L) maintained above T_L	60-150s
Peak package body temperature (T_p)	must not exceed the Classification temp T_c[1]
Time (t_p)* within 5 °C of the specified classification temperature (T_c)	30* seconds [2]
Ramp-down rate (T_p to T_L)	6 °C/second max.
Time 25 °C to peak temperature	8 minutes max.

*   [1] T_c=260°C.

*   [2] The time above 255 °C must not exceed 30 seconds.

Figure 11 Reflow soldering profile (Refer to IPC/JEDEC J-STD-020E)

8   PRODUCT PACKAGING AND HANDLING

8.1   Packaging

8.1.1   Packaging Notes

TAU1202/TAU1205 GNSS module is a Moisture Sensitive Device (MSD) and Electrostatic Sensitive Device (ESD). During the packing and shipping, it is strictly required to take appropriate MSD handling instructions and precautions. The table below shows the general packing hierarchy for the standard shipment.

Table 13 Packing hierarchy

Module	Reel	Sealed bag	Shipping carton

Note: Packaging of non-standard quantities is not explained here. Take the reality as a reference.

8.1.2   Tape and Reel

The TAU1202/TAU1205 modules are delivered as hermetically sealed, reeled tapes in order to enable efficient production, production lot set-up and tear-down. The figure below shows the tape dimension.
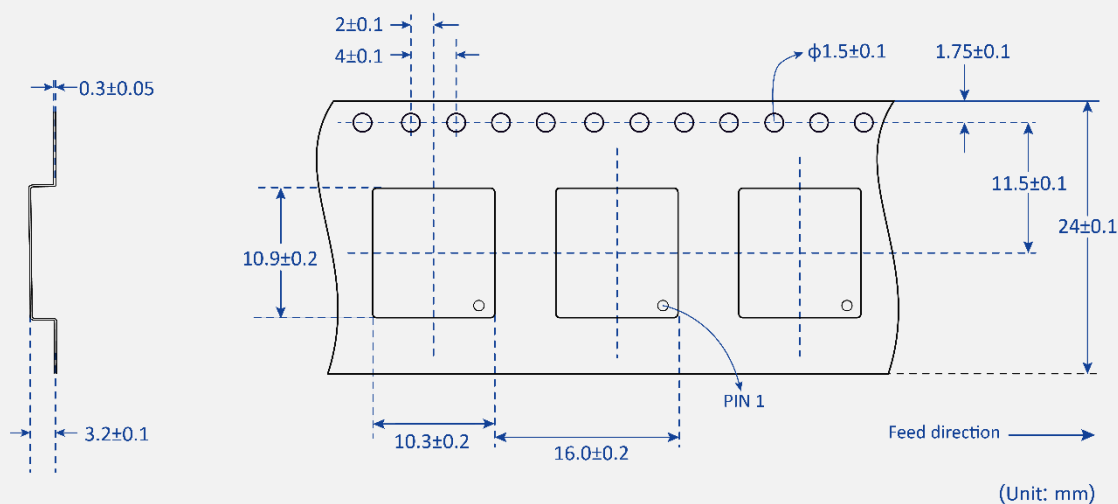

Figure 12 Tape dimensions

TAU1202/TAU1205 are deliverable in quantities of 1000pcs on a reel. The figure below shows the dimensions of reel for TAU1202/TAU1205.

(Unit: mm)
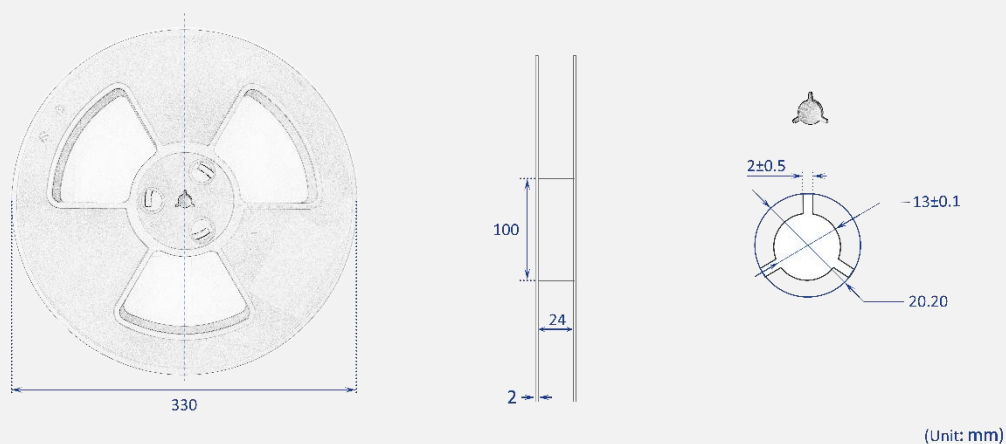
Figure 13 Reel dimensions

8.1.3 Shipment Packaging

The reels of TAU1202/TAU1205 modules are packed in the sealed bags and shipped by shipping cartons. Up to five sealed bags (5000pcs in total) can be packed in one shipping carton.

1000 pcs in a reel

Each bag contains a reel, desiccant and humidity indicator card.

5 bags in a carton

carton

350x350x200mm

Figure 14 Packaging

8.2   Storage

In order to prevent moisture intake and protect against electrostatic discharge, TAU1202/TAU1205 is packaged together with a humidity indicator card and desiccant to absorb humidity.

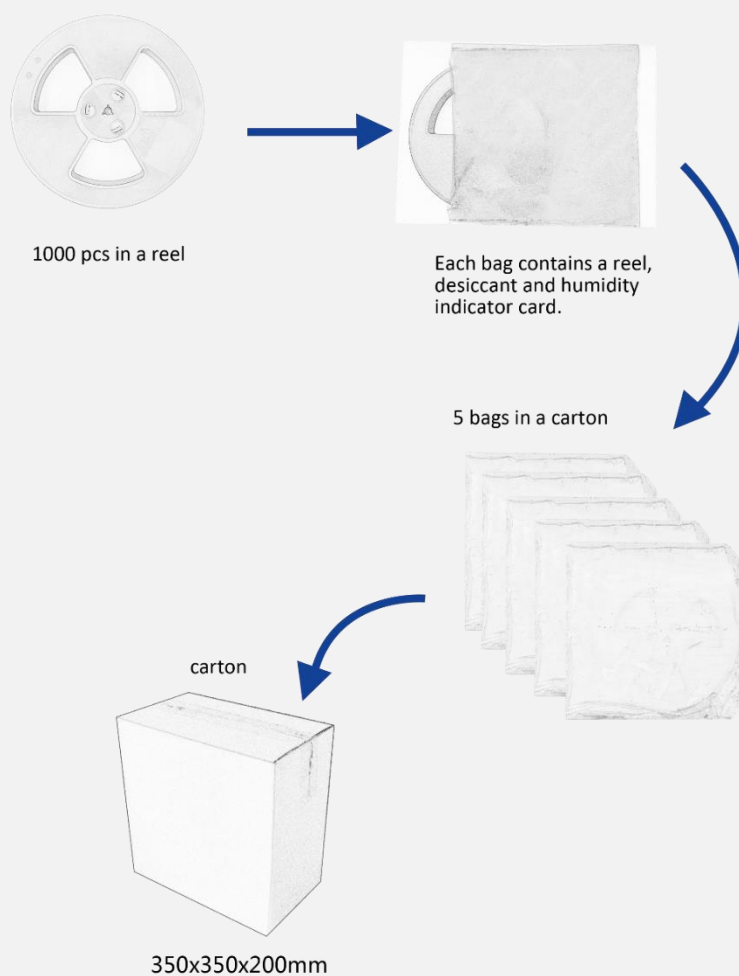## 8.3   Handling

8.3.1   ESD Handling Precautions

TAU1202/TAU1205 module which contains highly sensitive electronic circuitry is Electrostatic Sensitive Device (ESD). Observe precautions for handling! Failure to observe these precautions may result in severe damage to the GNSS module!

- Unless there is a galvanic coupling between the local GND (i.e. the workbench) and the PCB GND, then the first point of contact when handling the PCB must always be between the local GND and PCB GND.
- Before mounting an antenna patch, connect ground of the device.
- When handling the RF pin, do not come into contact with any charged capacitors and be careful when contacting materials that can develop charges (e.g. patch antenna ~10 pF, coax cable ~50 – 80 pF/m, soldering iron…)
- To prevent electrostatic discharge through the RF input, do not touch any exposed antenna area. If there is any risk that such exposed antenna area is touched in non ESD protected work area, implement proper ESD protection measures in the design.
- When soldering RF connectors and patch antennas to the receiver's RF pin, make sure to use an ESD safe soldering iron (tip).

ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING

ELECTROSTATIC
SENSITIVE
DEVICES

8.3.2   ESD protection measures

This series of GNSS positioning modules is sensitive to static electricity. Whenever handling the module, particular care must be exercised to reduce the risk of electrostatic charges. In addition to standard ESD safety practices, the following measures should be taken into account.

- Adds ESD Diodes to the RF input part to prevent electrostatics discharge.
- Do not touch any exposed antenna area.
- Adds ESD Diodes to the UART interface.

8.3.3   Moisture sensitivity level

The Moisture Sensitivity Level (MSL) of the GNSS modules is MSL4.

9　REVISION HISTORY

Revision	Date	Reviser	Status / Comments
V1.0	2019-05	Daisy	Start version, first released
V1.1	2019-09	Vita Wu	Logo, product photos and wording update
V1.2	2019-12	Vita Wu	Adds packaging info in Section 7; Updates mechanical specification; Updates sensitivity and power consumption in Table 1 and Table 7; Updates antenna gain in Section 4.2; Adds PCB package reference and layout notes in Section 6; Updates Section 6.1;
V1.3	2020-07	Vita Wu	Adds ANT_BIAS info; Adds built-in chock design, and updates reference design; Adds reflow soldering info; Updates mechanical spec; Simplifies Pin description; Other updates;
V1.4	2020-12	Vita Wu	Updates MSL. Improves wordings. Clarifies power on/off sequence. Updates AVDD_BAK connection description in Section 4.1. Updates D-typ and H value in table 11. Localization. Updates layout notes. Deletes 1K resistor in the minimal design diagram. Deletes SBAS support. Updates description about short circuit protection

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